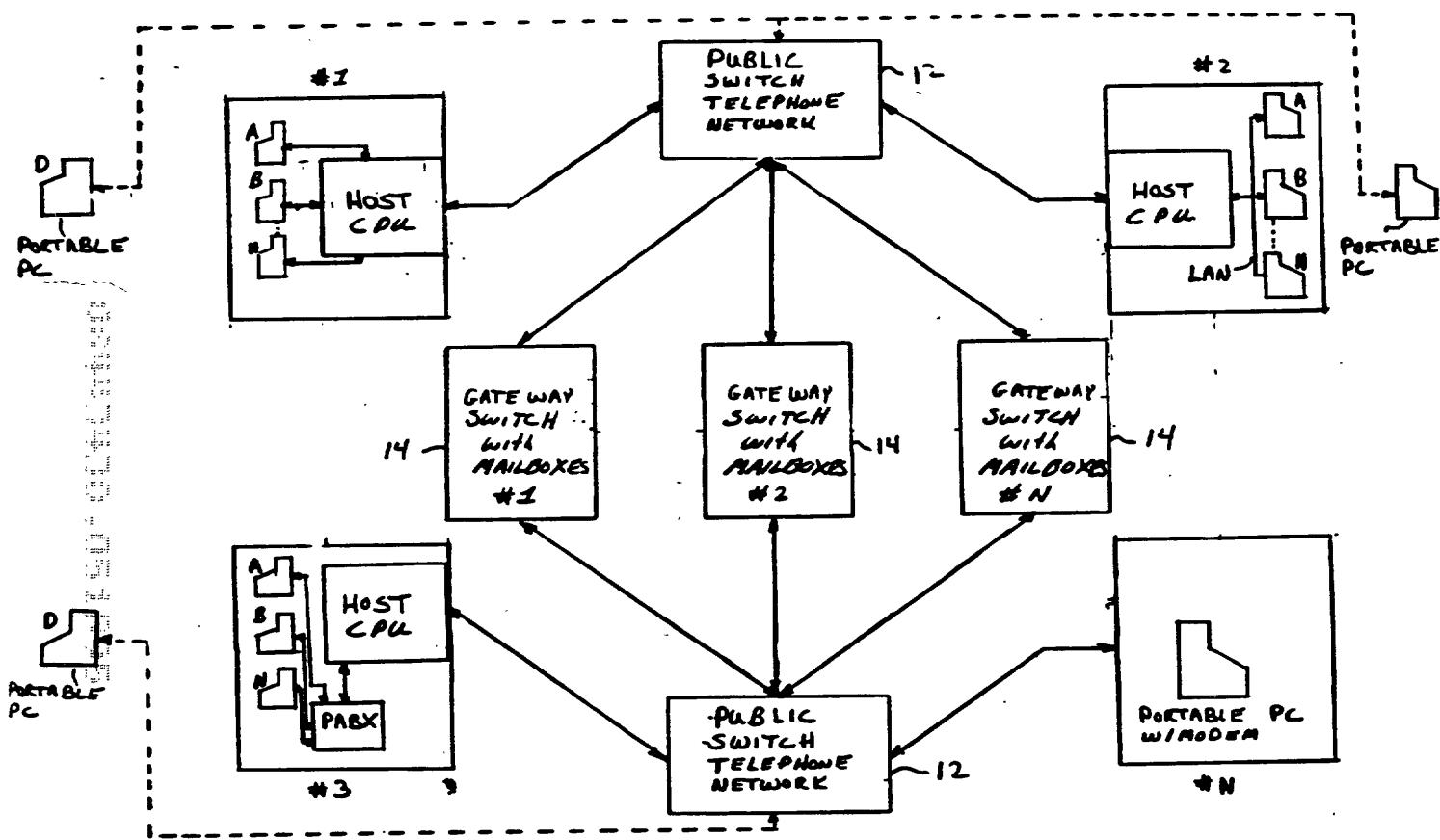


Fig. 1
(PRIOR ART)



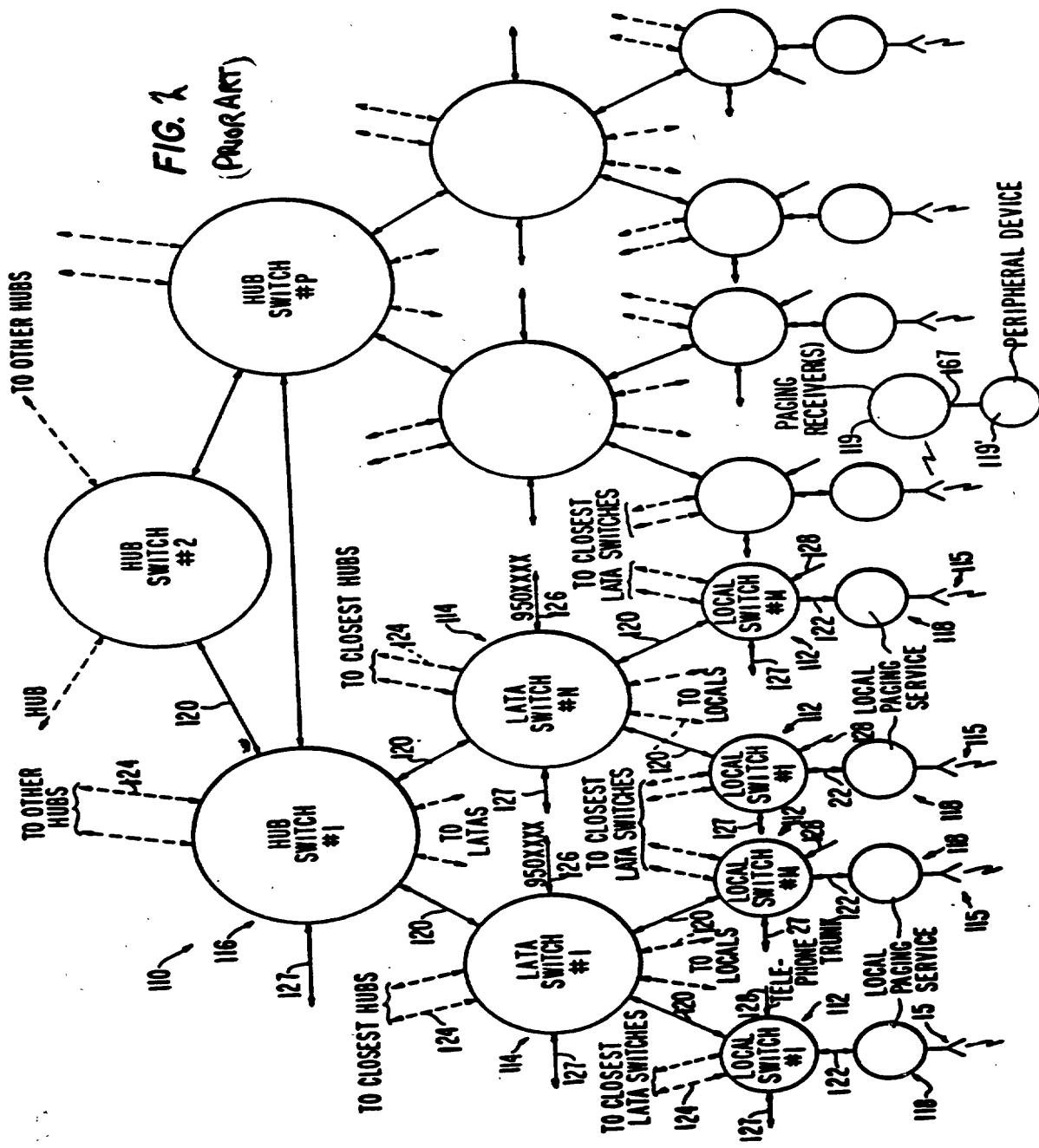
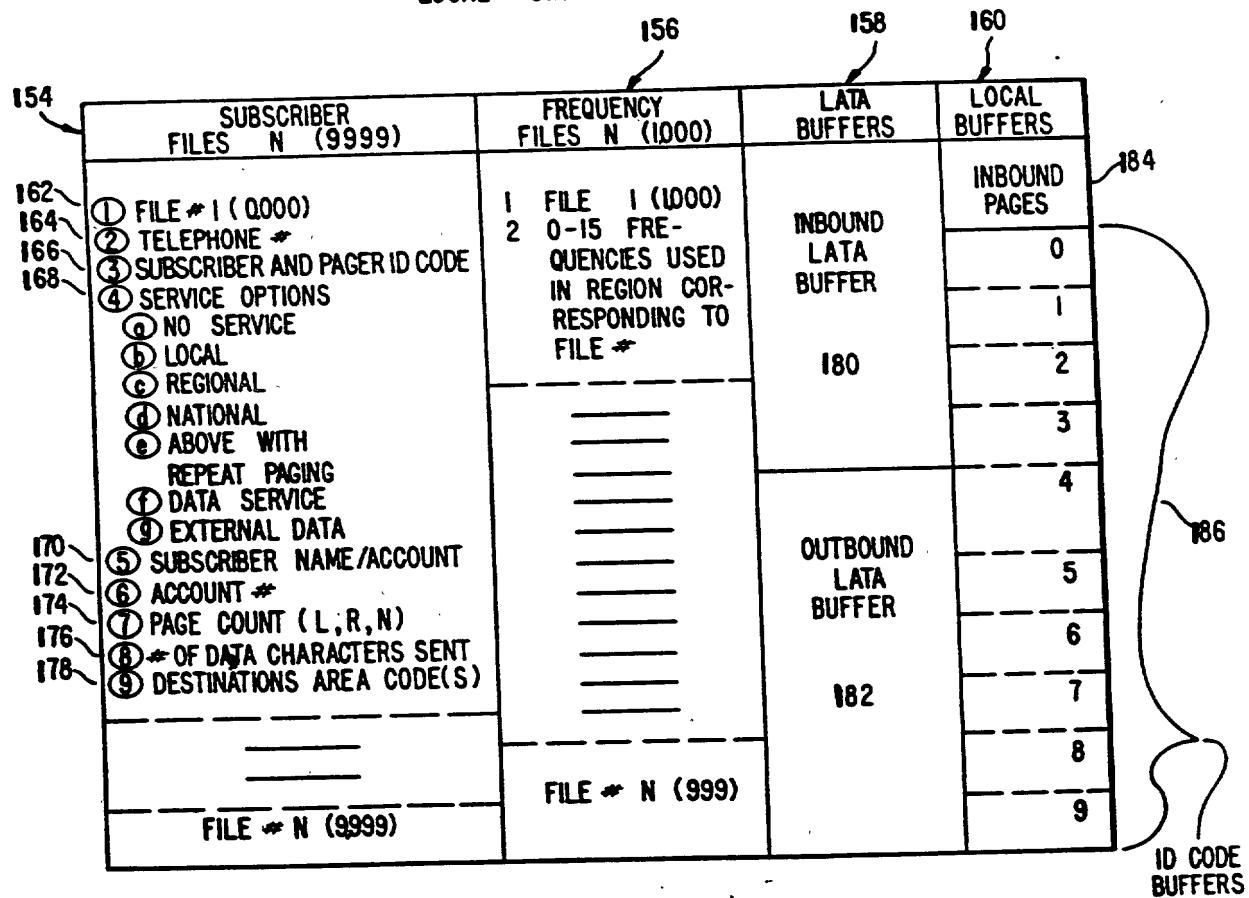


FIG. 3

(PRIOR ART)

LOCAL SWITCH MEMORY MAP



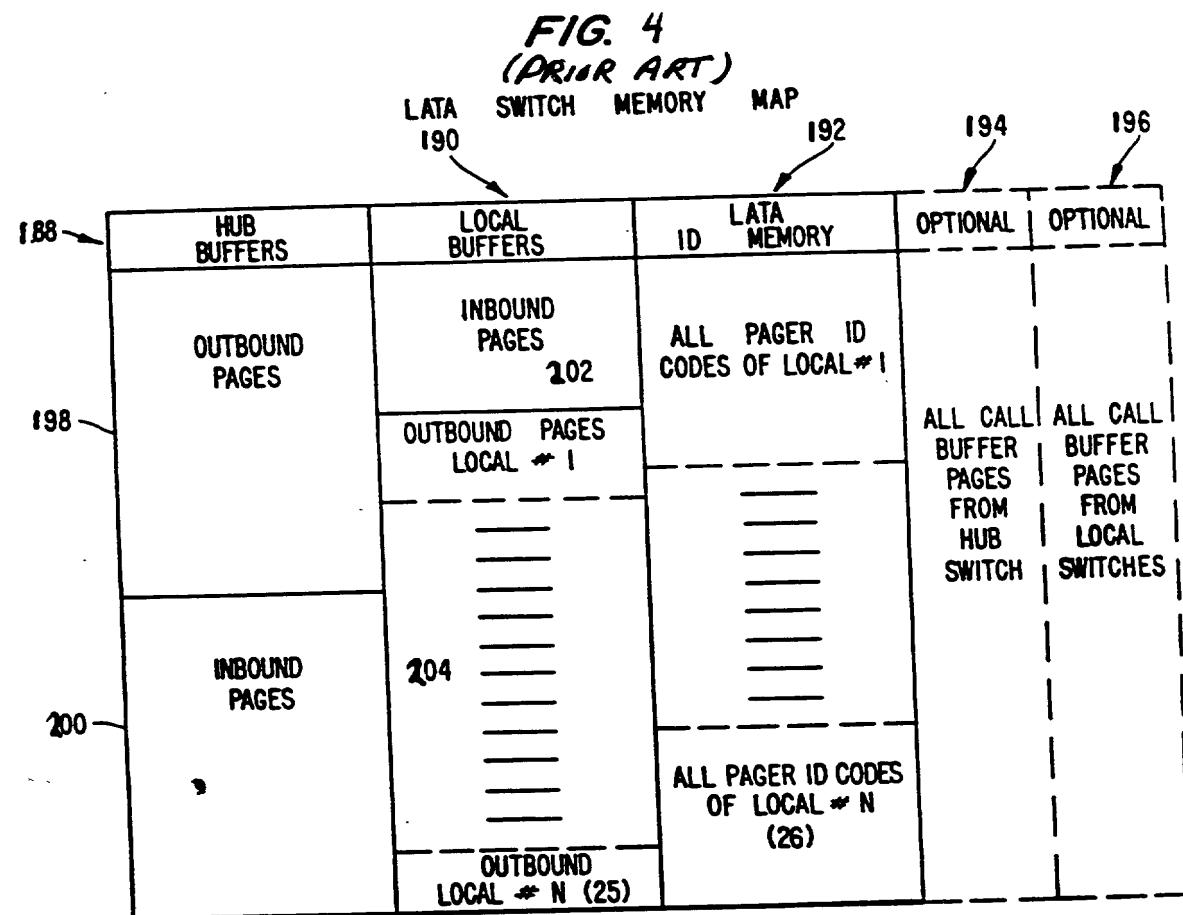


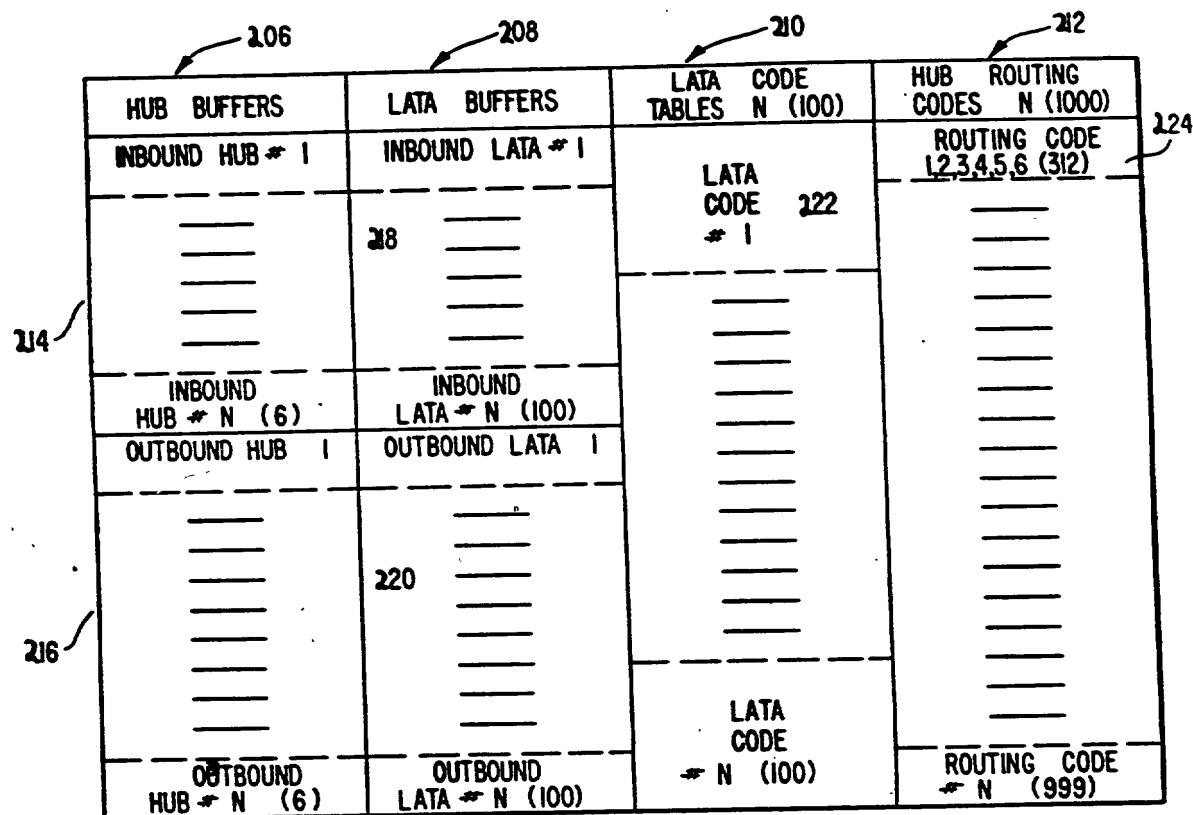
FIG. 5
(PRIOR ART)
HUB SWITCH MEMORY MAP

FIG. 6
(PRIOR ART)

THE FIVE LAYER MODIFIED X.25 PACKET

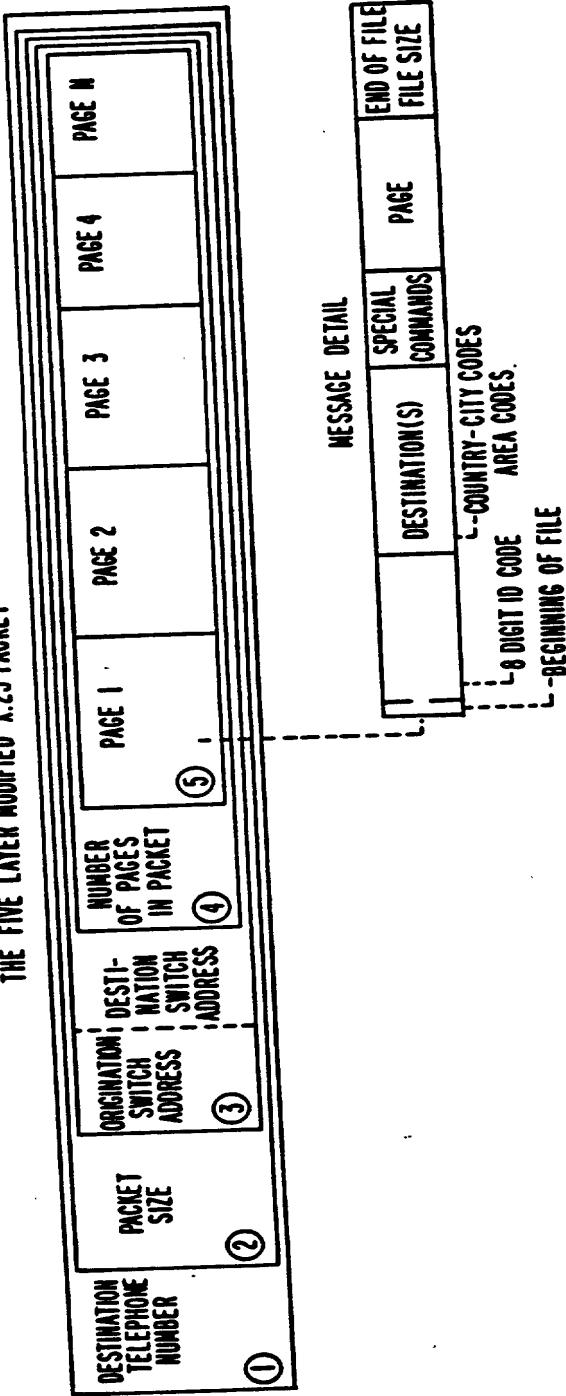
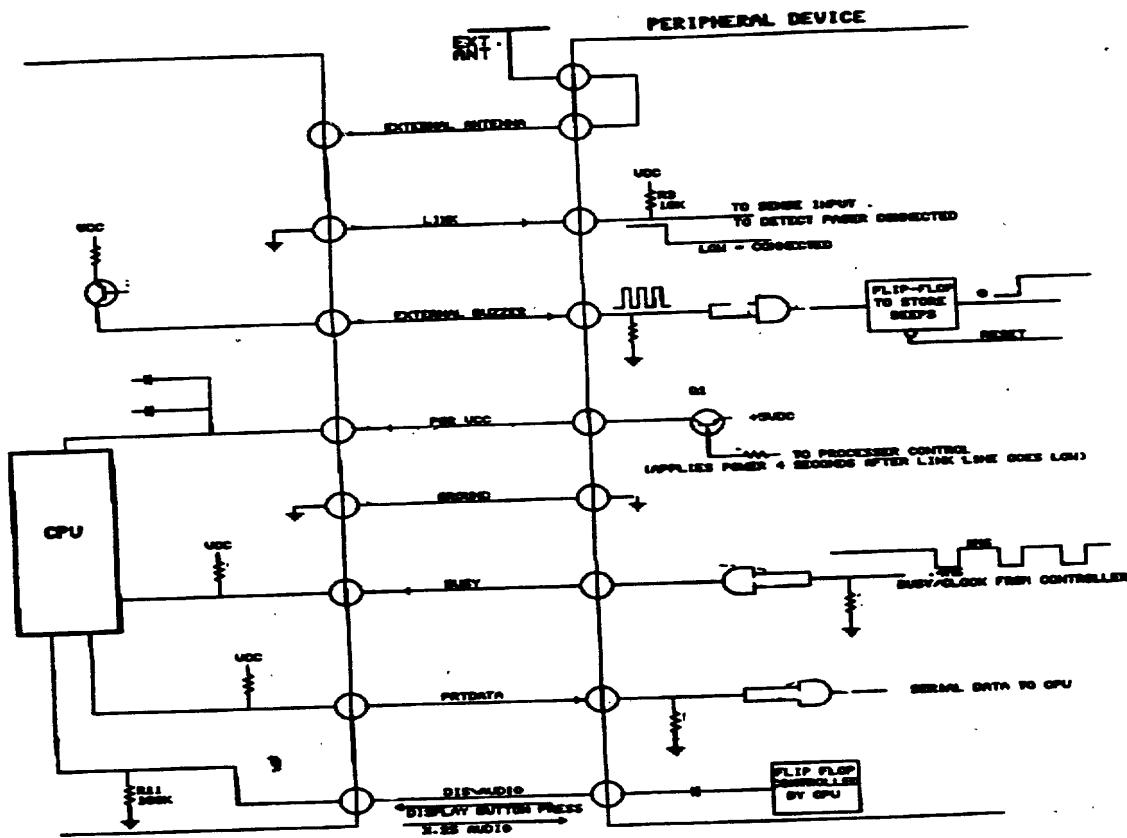
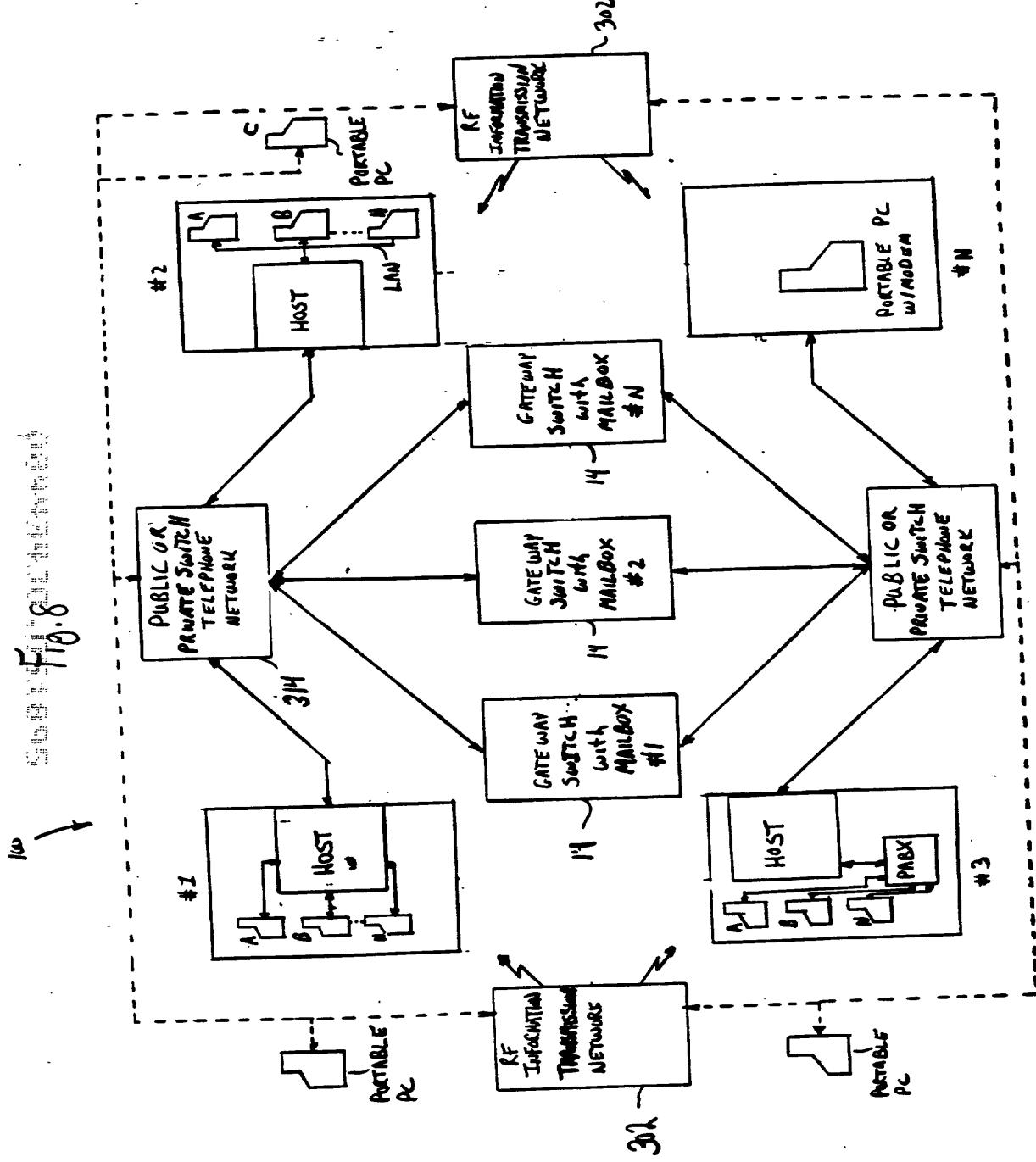


Fig. 7

(PRIOR ART)

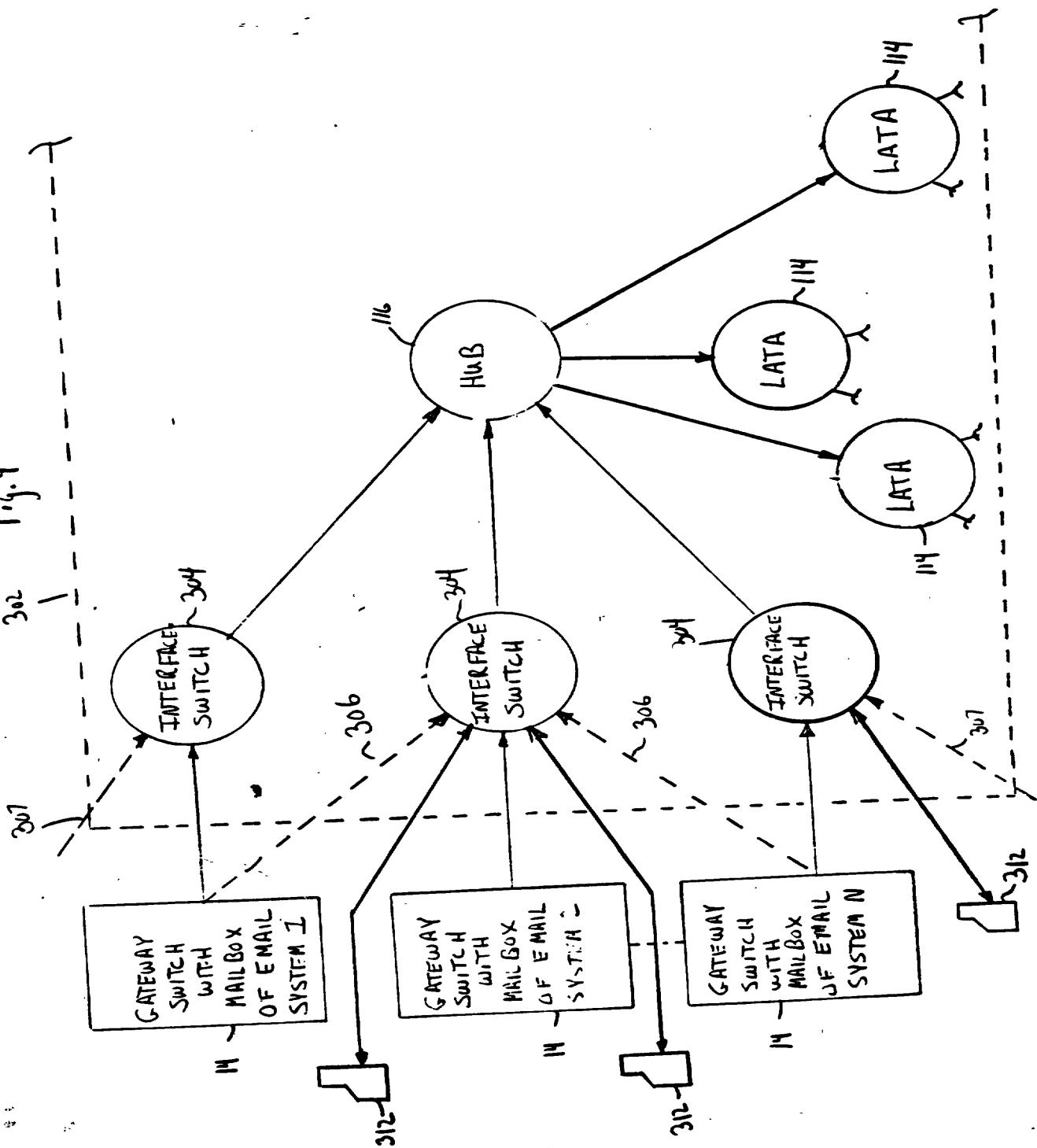
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Fig. 9

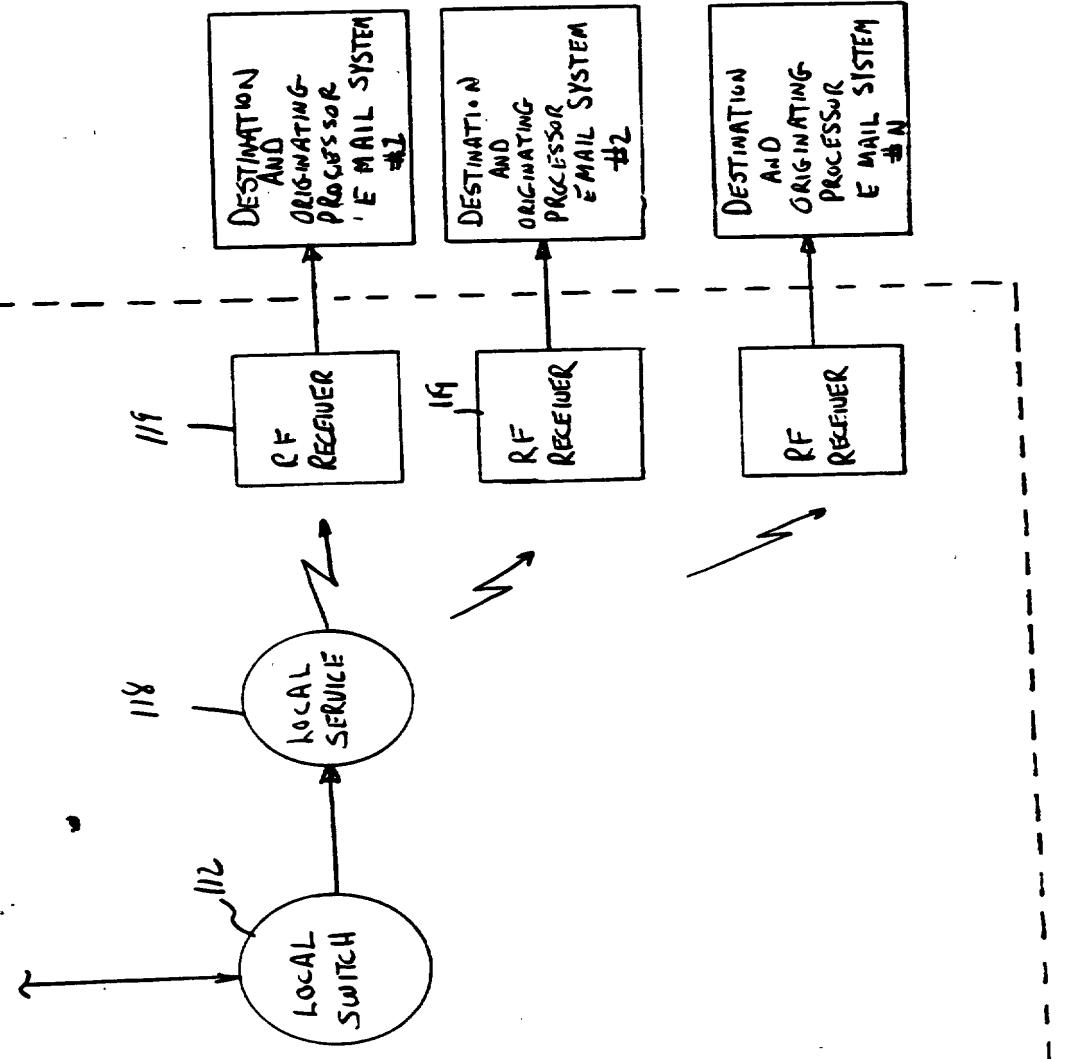


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FROM DATA SWITCH 114



ELECTRONIC MAIL MESSAGE ENTRY METHODS

Fig. 11

Entry Method	Originating Processor	Gateway Switch 1	Interface Switch 3-4
1	Adds interface (wireless) destination and destination processor	No-Action	Adds ID of RF receiver 1#
2	Adds interface (wireless) destination and ID of receiver 1#	No-Action	No action other than ID verification.
3	Adds destination processor	Adds wireless destination.	Adds ID of receiver 1#
4	Adds destination processor	Adds wireless destination and ID of receiver 1#	No action other than ID verification.
5	Adds destination processor, pointer points to displayed icon, processor adds wireless destination.	Adds ID of receiver 1#	No-Action other than ID verification.
6	Adds destination processor, pointer points to displayed icon, processor adds wireless destination.	No-Action	Adds ID of receiver 1#
7	Adds destination processor, pointer points to displayed icon, originating processor adds wireless destination and ID by comparing destination processor to ID table.	No-Action other than ID verification.	No-Action other than ID verification.

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Fig 12

